

FIG. 1

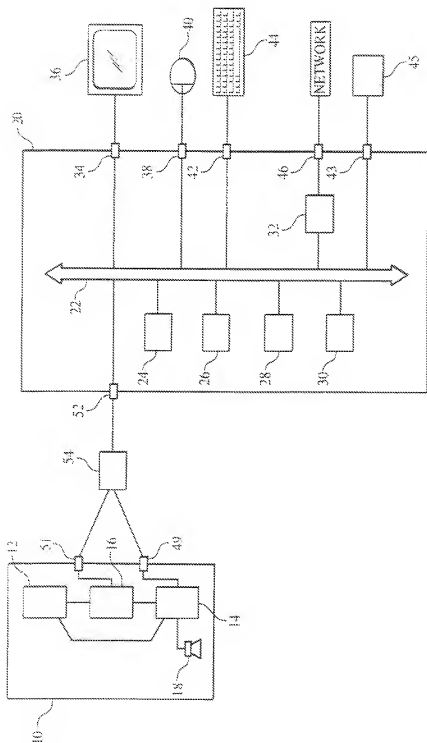


FIG. 2

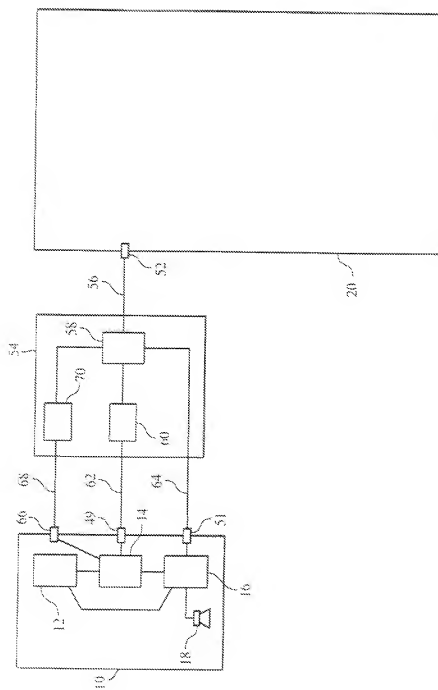


FIG. 3

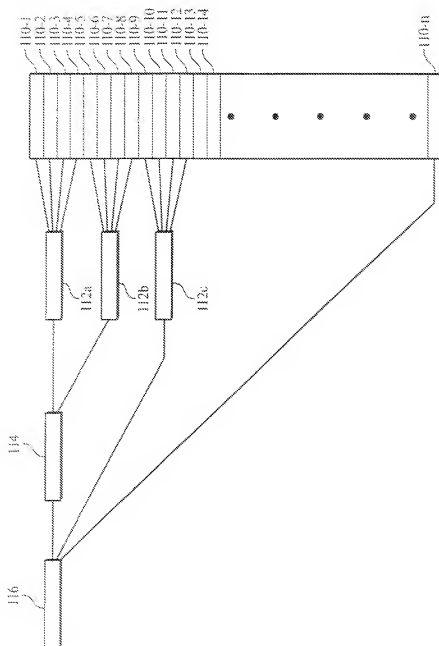


FIG. 4

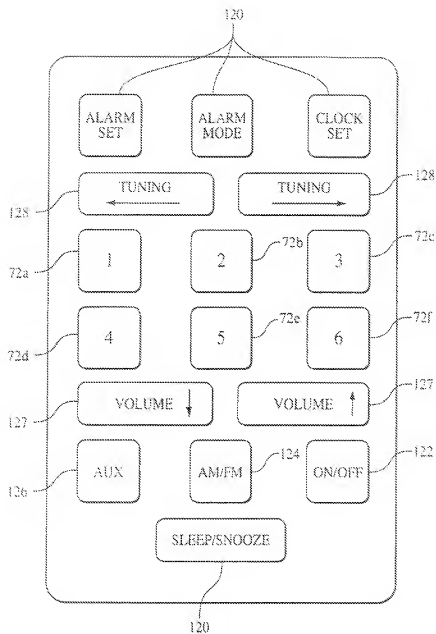


FIG. 5



FIG. 6

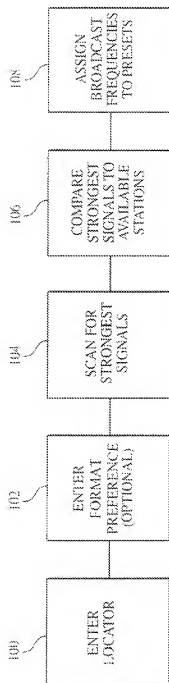


FIG. 7

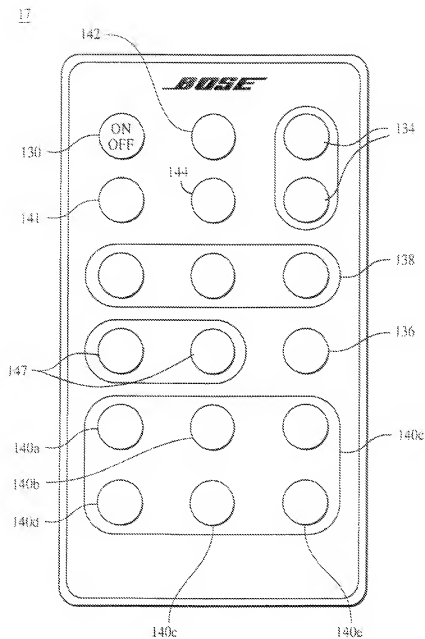


FIG. 8

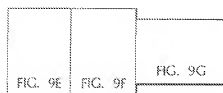
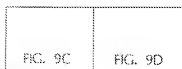
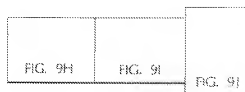
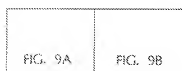


FIG. 9

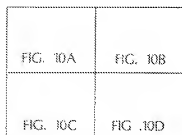
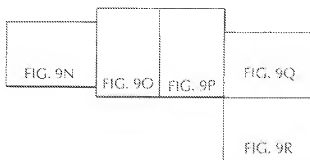
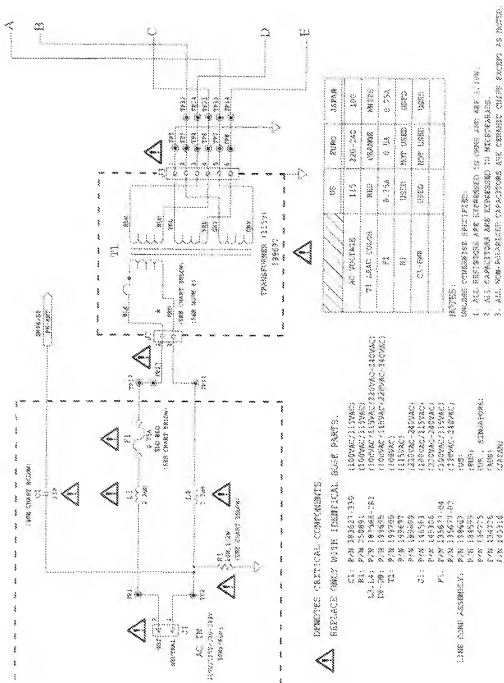


FIG. 10



REPLACE ONLY WITH IDENTICAL PARTS.

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FIG. 9A

- NOTES:
1. ALL DIMENSIONS ARE EXPRESSED IN INCHES AND ARE 1/16".
 2. ALL CAPACITORS ARE EXPRESSED IN MICROFARADS.
 3. ALL NON-POLARIZED CAPACITORS ARE CERAMIC UNLESS NOTED AS NOTED.
 4. CERAMIC CHIP CAPACITORS LESS THAN 1000 P.F. ARE 0.1%.
 5. CERAMIC CHIP CAPACITORS FROM 1000 TO 10,000 P.F. ARE 1%.
 6. CERAMIC CHIP CAPACITORS FROM 10,000 TO 100,000 P.F. ARE 5%.
 7. POLARITY OF TRANSFORMER AT J1 IS OPTIMUM.
 8. ALL VALUES SHOWN IN SCHEMATIC ARE FOR IN VERSION.

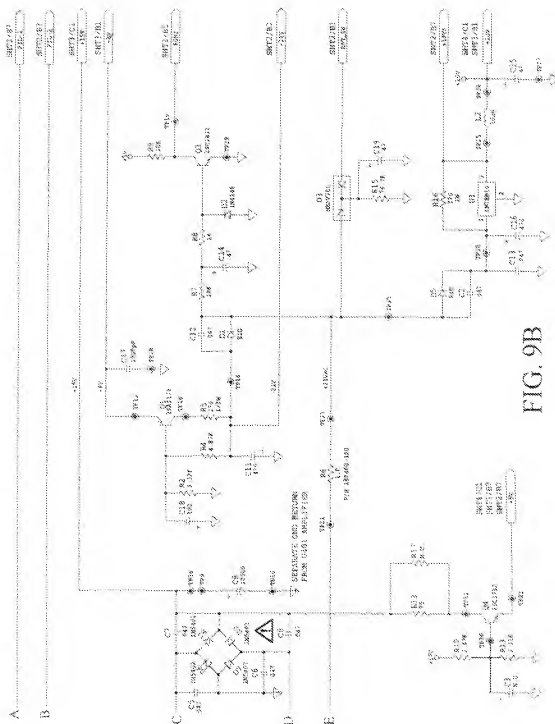
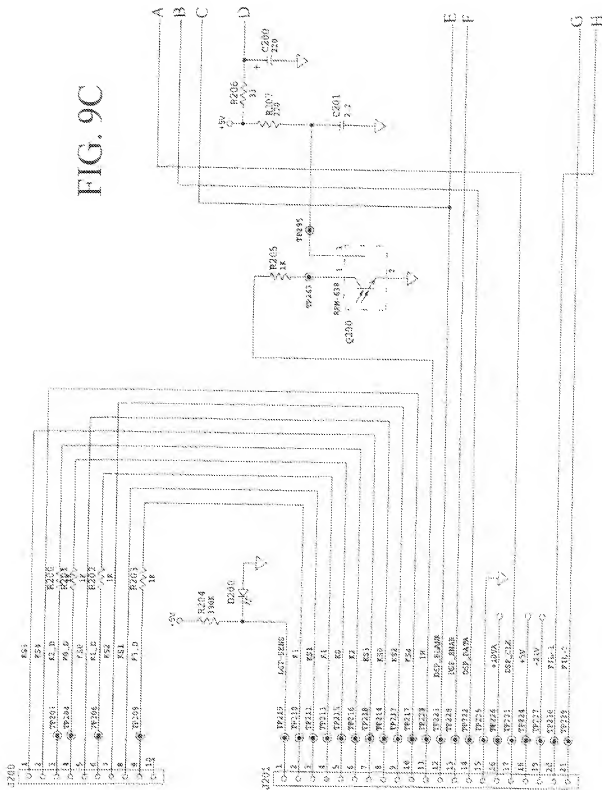
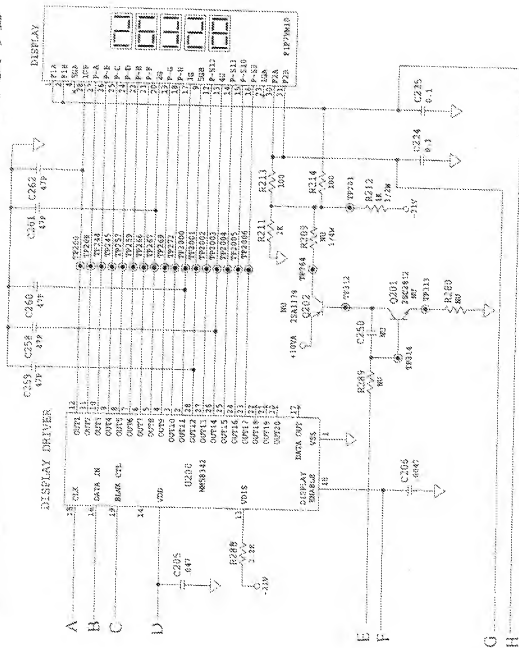


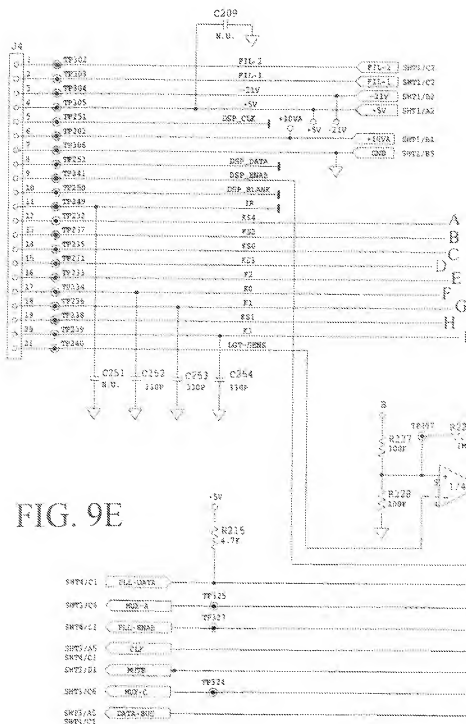
FIG. 9B

FIG. 9C



DISPLAY BOARD





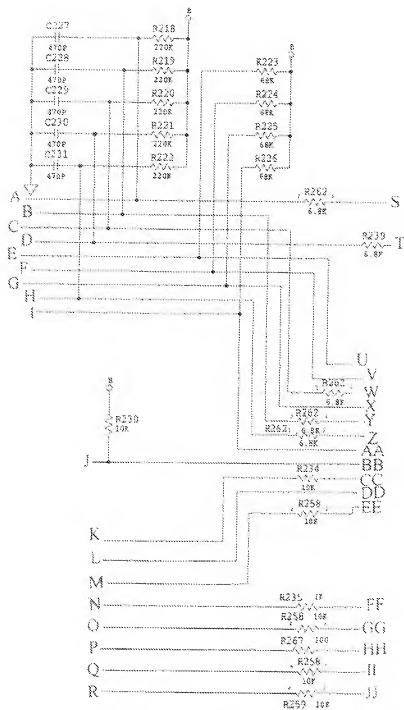


FIG. 9F



FIG. 96

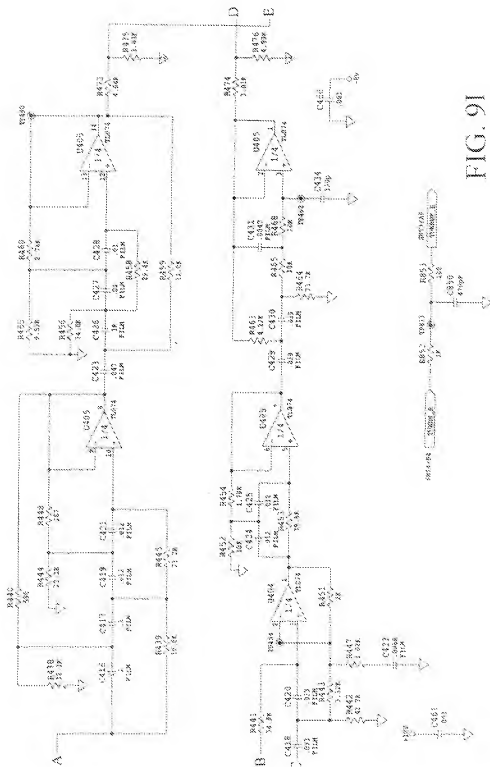


FIG. 9I



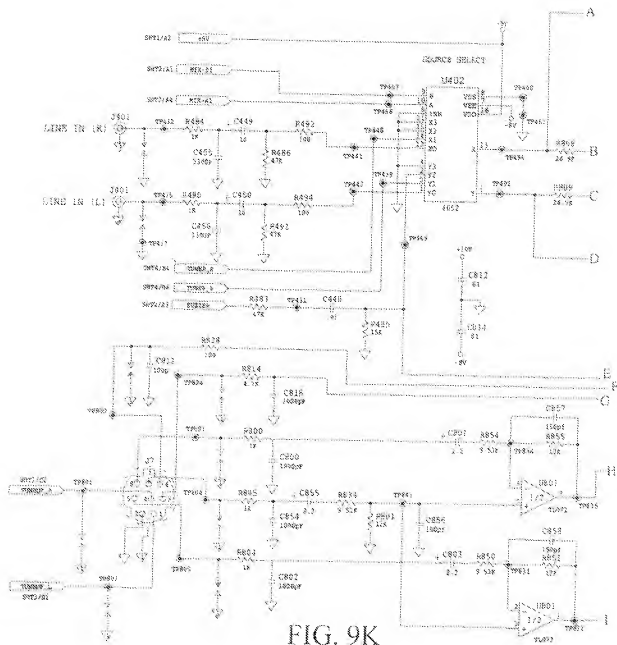


FIG. 9K

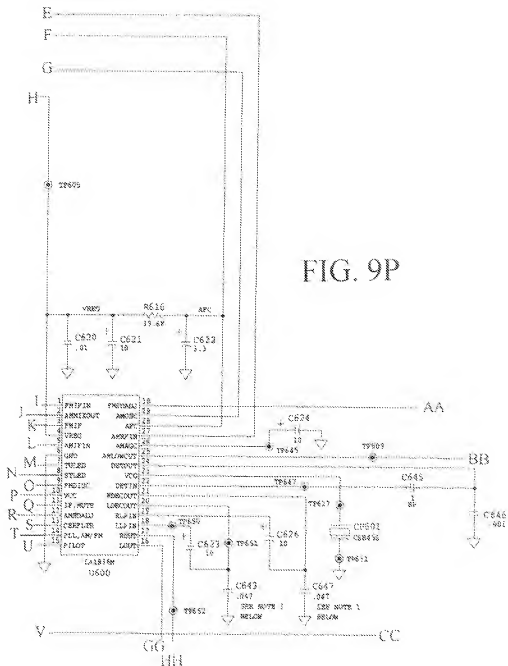








FIG. 9P



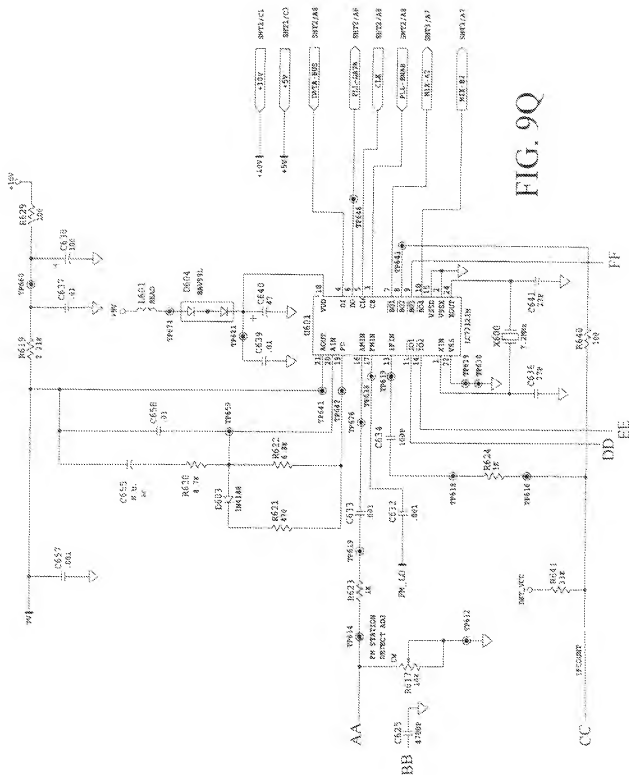
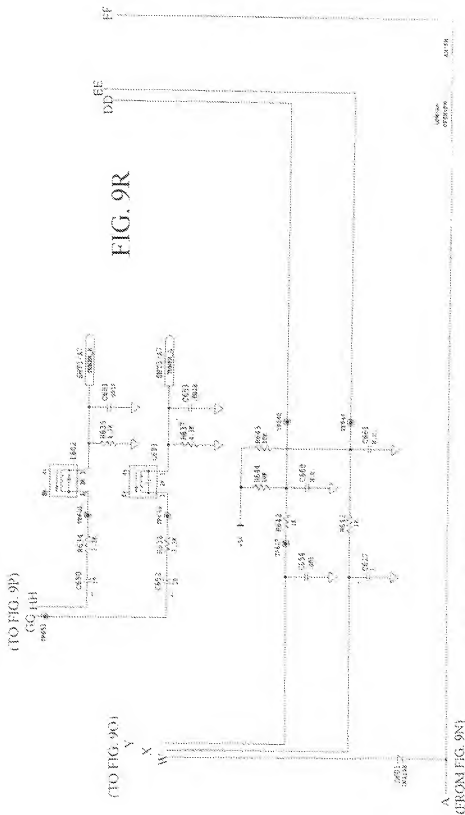


FIG. 9Q



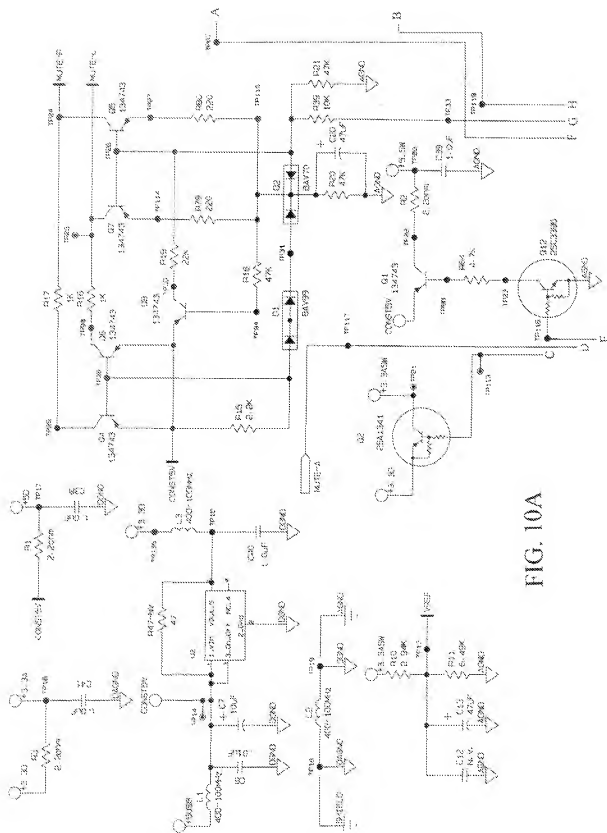


FIG. 10A

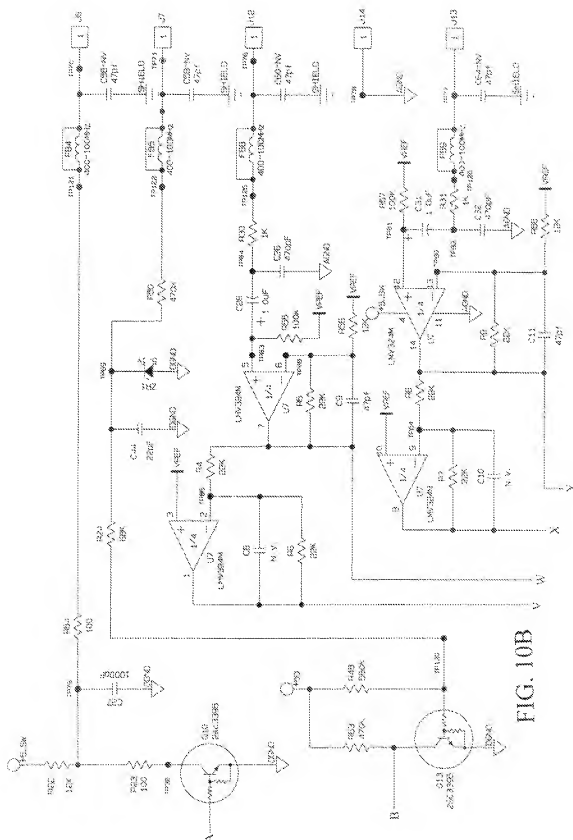


FIG. 10B

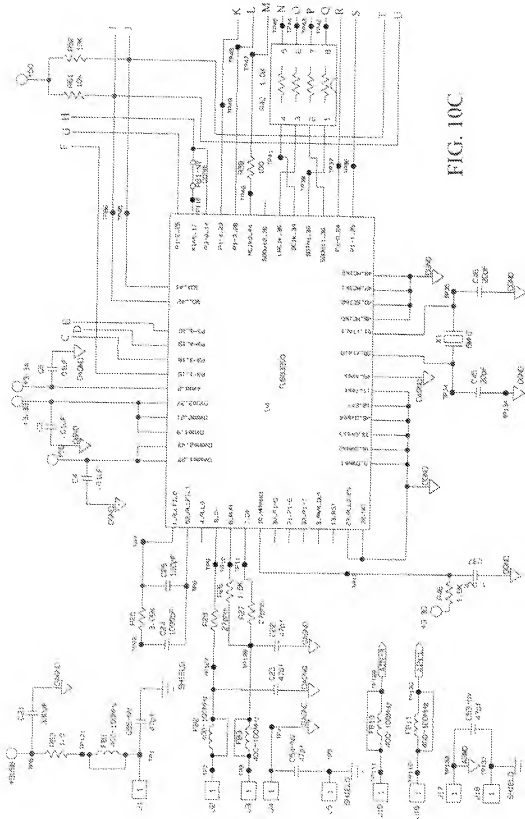


FIG. 10C

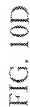


FIG. 10

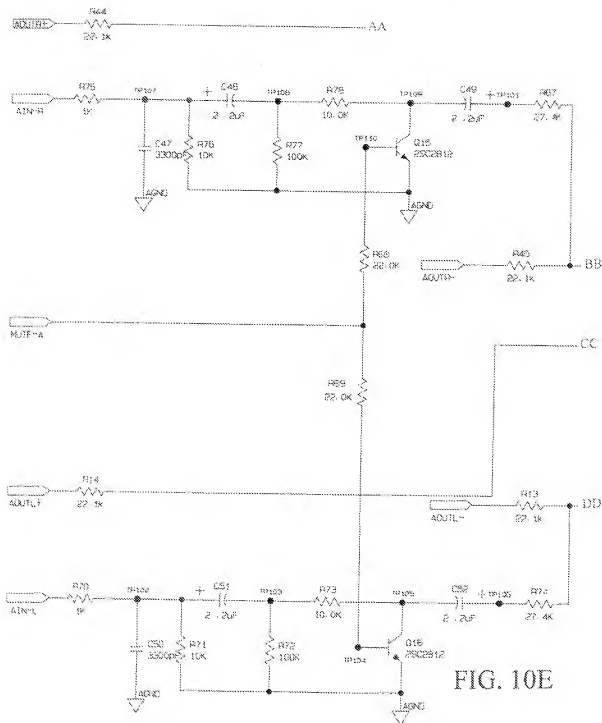


FIG. 10E

